

In the Specification:

Please replace the paragraph beginning on page 11, line 16, with the following amended paragraph:

41 As shown in Fig.2, the servo gate generator 6 comprises a time setting register 20, a counter 21, a comparison unit 22, and a gate generation unit 23. The time setting register 20 ~~sets~~ is set to the time ~~times~~ at which the servo gate will be generated from the micro-controller 11. The counter 21 counts the number of reference clock pulses. The comparison unit 22 compares the time set in register 20 and the value of the counter 21 and produces a matched output when the two match. The comparison unit 22 sends reset data to the counter 21 when the set time and the counted value match. In response to the matched output from the comparison unit 22, the gate generation unit 23 generates a servo gate signal.

Please replace the paragraph beginning on page 12, line 27, with the following amended paragraph:

F2 Memory 16 is connected to this micro-controller 11. The memory 16 stores the time discrepancy that shows the servo gate time discrepancies between each magnetic head and the standard head. Here, the standard head is head 0. The discrepancies between the servo gate times for each head 1, 2, and 3, and the standard head 0 are stored. For example, as shown in Fig. 5, the discrepancy between the servo gate times of head 1 and head 0 is ~~Tb~~ Td.

Please replace the paragraph beginning on page 15, line 19, with the following amended paragraph:

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In this way, the servo gate signals are synchronized at the time of the servo signal for the head to which switching is directed. Therefore, the time discrepancies between servo signals for all heads are stored and the time difference between the servo signals of current head and the servo signals of head to which switching is directed is calculated. The time at which the servo gate signal is generated is then synchronized with this time difference. Fig. 5 shows the relationship between the servo signals for each head when heads are switched from head 0 to head 1 and the servo gate signals. In this example, the time discrepancy for head 0  $T_a$  is "0" and the time discrepancy between head 0 and head 1 is  $T_b$   $T_d$ . The servo gate signal shown is for when the time difference  $T_d$  is smaller than the sample period  $T_s$ .

Please replace the paragraph beginning on page 23, line 19, with the following amended paragraph:

FC  
The control circuit 38 can measure the time discrepancy between the targeted head (for example, 1) and the standard head ~~head 9~~ head 0 by measuring the time between the two detection pulses. By appropriate selection of the head selector 37, the time discrepancy between head 2 and the standard head 0 and the time discrepancy between head 3 and the standard head 0 can be measured. These measurements are recorded in the memory 16 of

Fig. 2 or in the memory of Fig.7. Also, when the power is on, these measurements can be written to the magnetic disk track position first accessed by the head.

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